

50. The device of claim 48, wherein said hardware logic includes a plurality of sequencers corresponding to a plurality of headers of said protocol information.

51. A network interface device for processing communication between a network and a host having a stack of protocol layers, said device comprising:

a storage unit for storing a message packet being transferred between said network and said host, said packet including a sequence of protocol headers and associated data, and

a processor for referencing said headers to a context defining a network connection, and for moving said data according to said context between said storage unit and said host.

52. The network interface device of claim 51, wherein said processor includes a sequencer for validating and summarizing said packet.

53. The network interface device of claim 51, wherein said processor includes a plurality of pipelined microprocessors, with one of said microprocessors configured for transmitting network messages and another of said microprocessors configured for receiving network messages.

#### Remarks

Entry of the above Amendment is respectfully requested. Applicants' attorney apologizes for mistakenly listing claim 22 twice, and has cancelled the second claim 22. Claim 6 has been amended to depend from claim 1, and claims 1 and 2 have been amended to place the application in better condition for examination.


Respectfully submitted,

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